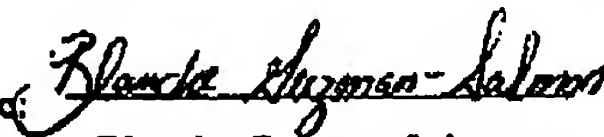RECEIVED
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Patent

Docket No.: Intel 2207/16369
Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh KOTTAPALLI et al.

SERIAL NO. : 10/650,335

FILED : August 28, 2003

FOR : METHOD FOR PAGE SHARING IN A PROCESSOR WITH
MULTIPLE THREADS AND PRE-VALIDATED CACHES

GROUP ART UNIT : 2189

EXAMINER : Daniel Bokmin KO

M/S: AMENDMENTS
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S: AMENDMENTS, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on
Dated: August 21, 2006   Signature: *Blanche Guzman-Salmon*
Blanche Guzman-Salmon

AMENDMENT

Dear Sir:

The following remarks are respectfully submitted in response to the Office

Action dated March 21, 2006.

A **Listing of the Claims** are reflected in the listing of claims which begins on page 2

of this paper.

Remarks/Arguments begin on page 9 of this paper.